

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: <b>DeWitt, Jr. et al.</b>	§
Serial No.: <b>Not Assigned</b>	§ Group Art Unit: <b>Not Assigned</b>
Filed: _____	§
	§ Examiner: <b>Not Assigned</b>
For: <b>Method and Apparatus for Identifying</b>	§
<b>False Cache Line Sharing</b>	§ Attorney Docket No.: <b>AUS920030554US1</b>

## INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.97

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants request that the information listed on the attached Form PTO-1449 be considered by the Office during the pendency of the above entitled application, pursuant to 37 C.F.R. 1.97.

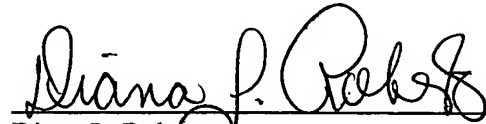
Please charge any fees necessary for prosecution of the present application to Deposit Account No. 09-0447. If any extension of time is required, such extension is hereby requested. Please charge any additional required fee for extension of time to Deposit Account No. 09-0447.

In accordance with 37 C.F.R. 1.97(h), the filing of this Information Disclosure Statement shall not constitute an admission that any information cited therein is, or is considered to be, material to patentability as defined in 37 C.F.R. 1.56(b). In the interest of full and complete disclosure to the Office, some or all of the art cited herein may not be considered by Applicant(s) or the Undersigned to be material under the new standards of materiality defined in 37 C.F.R. 1.56(b), enacted March 16, 1992, but may be material under the old standard of materiality defined in 37 C.F.R. 1.56(a), last amended on November 28, 1988, or may merely be technical background which may be of interest to the Examiner. In accordance with 37 C.F.R. 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made.

This Information Disclosure Statement is being filed under 37 C.F.R. § 1.97(b) within three months of the filing date of the application, or before the mailing date of a first office action on the merits. No fee is required.

Date: 12/1/03

Respectfully submitted,

A handwritten signature in cursive script, appearing to read "Diana L. Roberts", written over a horizontal line.

Diana L. Roberts  
*Registration No. 36,654*  
IBM Corporation  
Intellectual Property Law  
11400 Burnet Road  
Austin, Texas 78757  
(512) 823-9667

<b>Form PTO-1449</b>  <b>LIST OF PRIOR ART CITED BY APPLICANT</b> <i>(Use several sheets if necessary)</i>			ATTORNEY DOCKET NO. <b>AUS920030554US1</b>		SERIAL NO. <b>Not Assigned</b>	
			APPLICANT <b>DeWitt, Jr. et al.</b>			
			FILING DATE		GROUP ART UNIT <b>Not Assigned</b>	
U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE	
	AA	5,103,394	Blasciak	395/575	Dec. 21, 1989	
	AB	6,330,662 B1	Patel et al.	712/236	Feb. 23, 1999	
	AC	6,480,938 B2	Vondran, Jr.	711/125	Dec. 15, 2000	
	AD	6,430,741 B1	Mattson, Jr. et al.	717/154	Feb. 26, 1999	
	AE	6,189,141 B1	Benitez et al.	717/4	May 4, 1998	
	AF	5,930,508	Faraboschi et al.	395/706	Jun. 9, 1997	
	AG	6,351,844 B1	Bala	717/4	Nov. 5, 1998	
	AH	6,324,689 B1	Lowney et al.	717/9	Sep. 30, 1998	
	AI	6,442,585 B1	Dean et al.	709/108	Nov. 26, 1997	
	AJ	5,774,724	Heisch	395/704	Nov. 20, 1995	
	AK	5,987,250	Subrahmanyam	395/704	Aug. 21, 1997	
	AL	6,192,513 B1	Subrahmanyam	717/5	Nov. 2, 1998	
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	AN	6,223,338 B1	Smolders	717/4	Sep. 30, 1998	
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	AT	6,243,804 B1	Cheng	712/228	Jul. 22, 1998	
	AU	4,291,371	Holtey	364/200	Jan. 2, 1979	
	AV	5,938,778	John, Jr. et al.	714/45	Nov. 10, 1997	
	AW	6,286,132 B1	Tanaka et al.	717/4	Jan. 7, 1999	
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	AY	6,374,364 B1	McElroy et al.	714/10	Jan. 19, 1999	
	AZ	6,070,009	Dean et al.	395/704	Nov. 26, 1997	
	BA	5,966,537	Ravichandran	395/709	May 28, 1997	
DATE CONSIDERED			EXAMINER			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.						

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	APPLICANT <b>DeWitt, Jr. et al.</b>	
	FILING DATE	GROUP ART UNIT <b>Not Assigned</b>

### U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/SUBCLASS	FILING DATE
	BB 2002/0129309	Sep. 12, 2002	Floyd et al.	714/724	Dec. 18, 2000
	BC 2001/0032305	Oct. 18, 2001	Barry	712/34	Feb. 23, 2001
	BD 2002/0199179	Dec. 26, 2002	Lavery et al.	717/158	Jun. 21, 2001
	BE 2002/0124237	Sep. 5, 2002	Sprunt et al.	717/127	Dec. 29, 2000
	BF 2002/0147965	Oct. 10, 2002	Swaine et al.	717/124	Feb. 1, 2001
	BG 2002/0019976	Feb. 14, 2002	Patel et al.	717/137	May 25, 2001

### FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/SUBCLASS	TRANSLATION YES NO
	BH JP2000029731	Dec. 8, 1999	Japan	G06F 9/38	<input type="checkbox"/> <input checked="" type="checkbox"/>
	BI JP2000347863	Dec. 15, 2000	Japan	G06F 9/38	<input type="checkbox"/> <input checked="" type="checkbox"/>

### OTHER PRIOR ART (including author, title, date, pertinent page, etc.)

	BJ	Kikuchi, "Parallelization Assist System", Joho Shori, Vol. 34, No. 9, Sept. 1993, pp. 1158-1169.
	BK	Cohen et al., "Hardware-Assisted Characterization of NAS Benchmarks", Cluster Computing, V I. 4, No. 3, July 2001, pp. 189-196.
	BL	Talla et al., "Evaluating Signal Processing and Multimedia Applications on SIMD, VLIW and Super Scalar Architectures", International Conference on Computer Design, Austin, Sept. 17-20, 2000, pp. 163-172.
	BM	Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processings Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.
	BN	Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19 <sup>th</sup> , Phoenix, Feb. 20-22, 2000, pp. 516-524.
	BO	IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance Measurement Tool for Software Development Cost Reduction", April 2001, pp. 686-688.

### RELATED PATENT APPLICATIONS

EXAMINER INITIAL	APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
	BP 09/435,069 AT9-99-491	Davidson et al.	Method and Apparatus for Instruction Sampling for Performance Monitoring and Debug	Nov. 4, 1999
	BQ 08/538,071 AUT919950055US1	Gover et al.	Method and System for Selecting and Distinguishing an Event Sequence using an Effective Address in a Processing System	Oct. 2, 1995

DATE CONSIDERED

EXAMINER

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.